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Section 1 **Introduction to Altera EPLDs** 1

Provides an introduction to Erasable Programmable Logic Devices (EPLDs). This section describes the principles underlying EPLD architecture, summarizes EPLD families and software tools offered by Altera, and explains the advantages of CMOS EPROM technology. This section also contains a Product Selection Guide for a quick overview of Altera products.

Section 2 **Classic EPLDs** 35

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